

**COMBINED DECLARATION AND POWER  
OF ATTORNEY FOR PATENT APPLICATION**

As one of the below-named inventors, I hereby declare that:

My residence, post office address and citizenship is as stated below next to my name.

I believe I am the first, original and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled HIGH SELECTIVITY SiC ETCH IN INTEGRATED CIRCUIT FABRICATION, the specification of which is filed concurrently herewith, and which is assigned Attorney Docket No. 01-099 by my below-named attorneys.

The persons named as inventors are: Rongxiang Hu, Philippe Schoenborn, and Masaichi Eda.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, § 1.56(a).

No priority claim is made under 35 U.S.C. § 119 or 35 U.S.C. § 120.

Power of Attorney: As the named inventors, we hereby appoint Sandeep Jaggi, Registration No. 43,331; Timothy Croll, Registration No. 36,771; Leo Peters, Registration No. 33,562; Pete Scott, Registration No. 33,279; John R. Ley, Registration No. 27,453; Bruce R. Winsor, Registration No. 34,281; and L. Jon Lindsay, Registration No. 36,855, to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

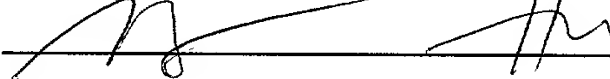
Send all correspondence to: Sandeep Jaggi, Intellectual Property Law Department, LSI Logic Corporation, M/S D-106, 1551 McCarthy Boulevard, Milpitas, CA 95035, and direct telephone calls to Sandeep Jaggi at (408) 954-4923.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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